

[IEEE HOME](#) | [SEARCH IEEE](#) | [SHOP](#) | [WEB ACCOUNT](#) | [CONTACT IEEE](#)[Membership](#) [Publications/Services](#) [Standards](#) [Conferences](#) [Careers/Jobs](#)Welcome
United States Patent and Trademark Of[Help](#) [FAQ](#) [Terms](#) [IEEE Peer Review](#) [Quick Links](#)

>> Sea

Welcome to IEEE Xplore

- [Home](#)
- [What Can I Access?](#)
- [Log-out](#)

Tables of Contents

- [Journals & Magazines](#)
- [Conference Proceedings](#)
- [Standards](#)

Search

- [By Author](#)
- [Basic](#)
- [Advanced](#)

Member Services

- [Join IEEE](#)
- [Establish IEEE Web Account](#)
- [Access the IEEE Member Digital Library](#)

 [Print Format](#)Your search matched **1 of 988757** documents.

A maximum of **1** results are displayed, **25** to a page, sorted by **Relevance in descending order**. You may refine your search by editing the current search expression or entering a new one the text

Then click **Search Again**.

transconduct\$ and (parallel or plural)

[Search Again](#)**Results:**Journal or Magazine = **JNL** Conference = **CNF** Standard = **STD****1 A low-voltage CMOS OTA with rail-to-rail differential input range***Li, M.F.; Dasgupta, U.; Zhang, X.W.; Yong Ching Lim;*

Circuits and Systems I: Fundamental Theory and Applications, IEEE Transaction

Volume: 47 Issue: 1 , Jan. 2000

Page(s): 1 -8

[\[Abstract\]](#) [\[PDF Full-Text \(152 KB\)\]](#) **IEEE JNL**[Home](#) | [Log-out](#) | [Journals](#) | [Conference Proceedings](#) | [Standards](#) | [Search by Author](#) | [Basic Search](#) | [Advanced Search](#)
[Join IEEE](#) | [Web Account](#) | [New this week](#) | [OPAC Linking Information](#) | [Your Feedback](#) | [Technical Support](#) | [Email Alerting](#)
[No Robots Please](#) | [Release Notes](#) | [IEEE Online Publications](#) | [Help](#) | [FAQ](#) | [Terms](#) | [Back to Top](#)

Copyright © 2003 IEEE — All rights reserved